

## C<sub>60</sub> as a versatile electron contact material for crystalline silicon solar cells

Yuchen Sun<sup>1</sup>, Jesus Ibarra Michel<sup>1</sup>, Daniela Bozanic<sup>1</sup>, Yumin Li<sup>1</sup>, Pheng Phang<sup>2</sup>, Heping Shen<sup>2</sup>, Jie Yang<sup>3</sup>, Peiting Zheng<sup>2</sup>, Xinyu Zhang<sup>3</sup>, Di Yan<sup>1</sup>, and James Bullock<sup>1</sup>

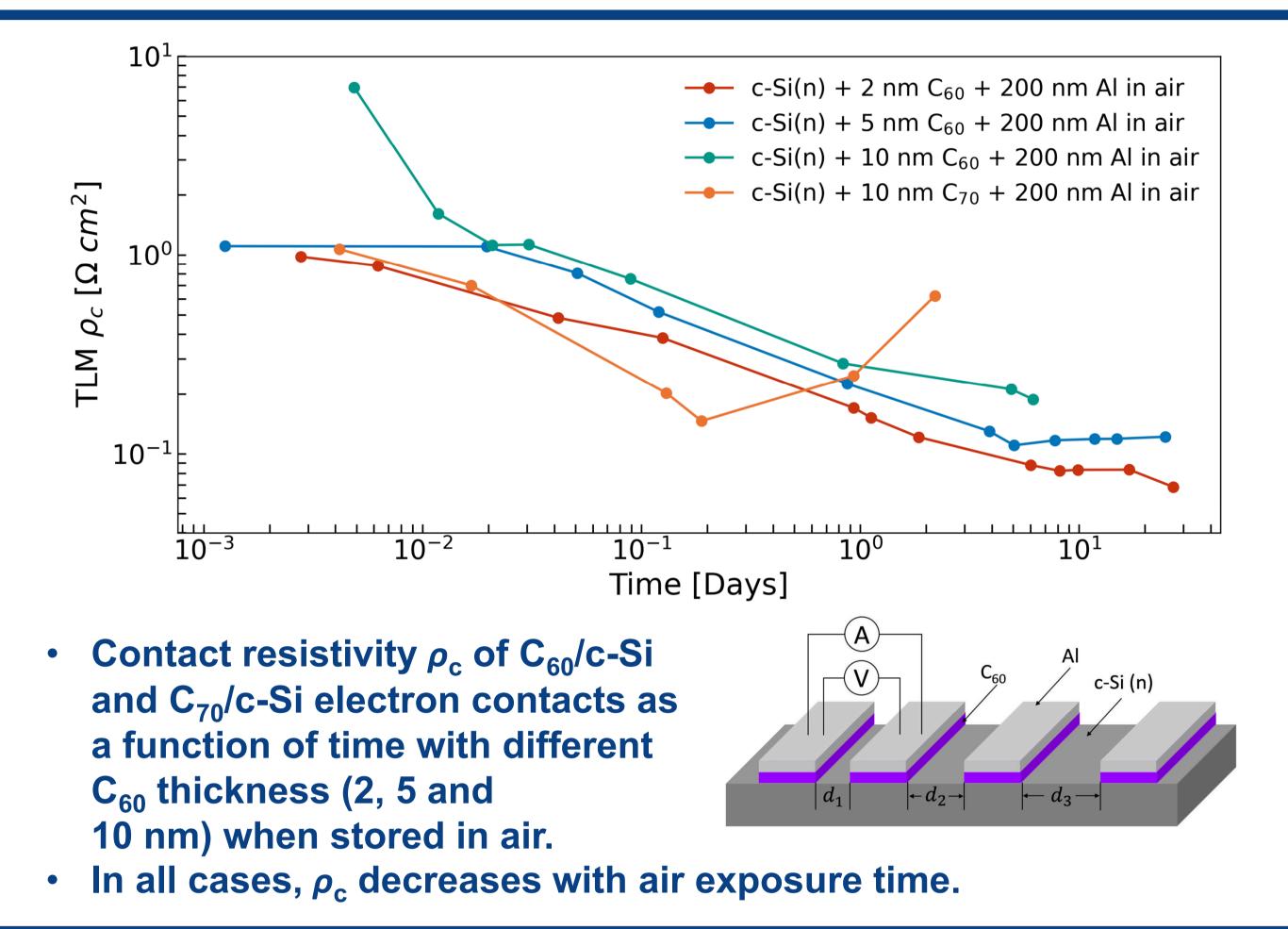
<sup>1</sup>Department of Electrical and Electronic Engineering, University of Melbourne, Parkville, VIC, 3052, Australia <sup>2</sup>School of Engineering, Australian National University, Canberra ACT 2601, Australia <sup>3</sup>Jinko Solar, Yingbin Avenue, Shangrao Economic Development Zone, Jiangxi, 330000, China

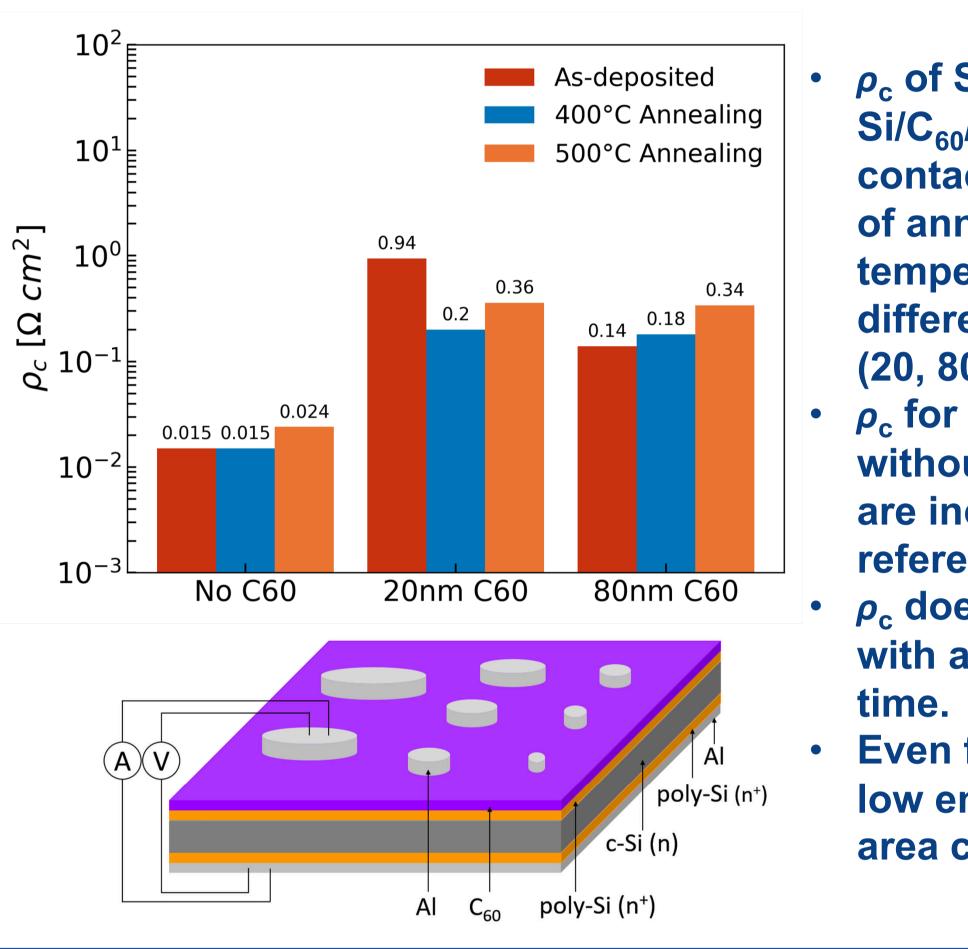
## Abstract

C<sub>60</sub> has been used as an electron contact in organic/perovskite solar cells and LEDs. However, it has not been thoroughly tested with crystalline silicon solar cells. In this study, we conducted tests on C<sub>60</sub>'s ability to perform two functions in c-Si solar cells. The first function is direct electron contacts to c-Si. The second is as a buffer or protection layer for ultrathin (~10 nm) polysilicon contacts.

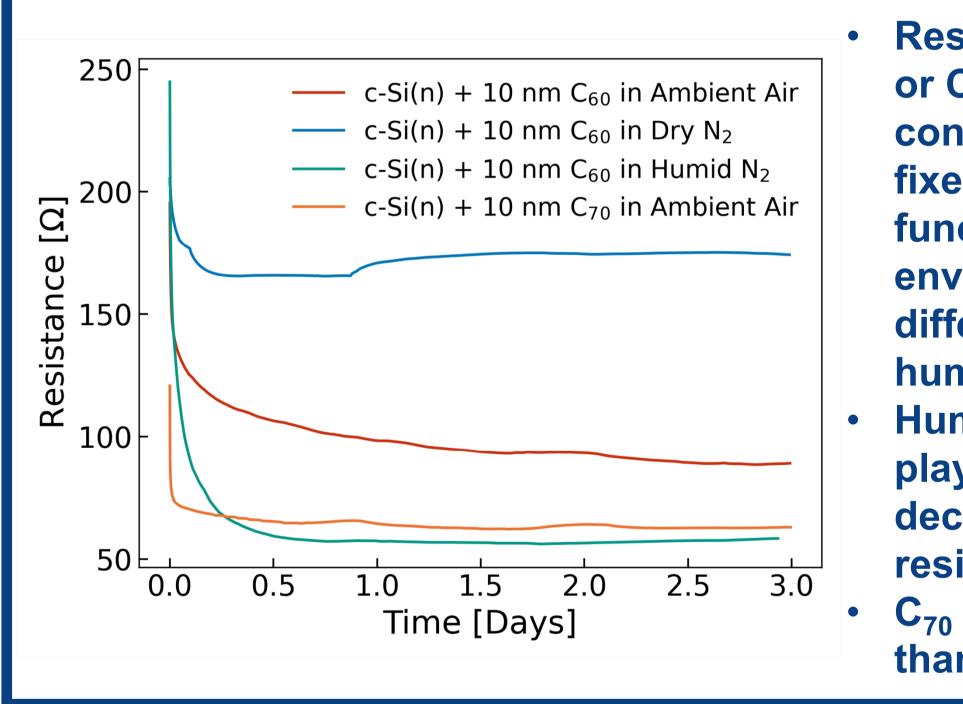
## **Application #1: Direct contact to c-Si**

## **Application #2: Barrier layer for** ultrathin (~10 nm) polysilicon contacts



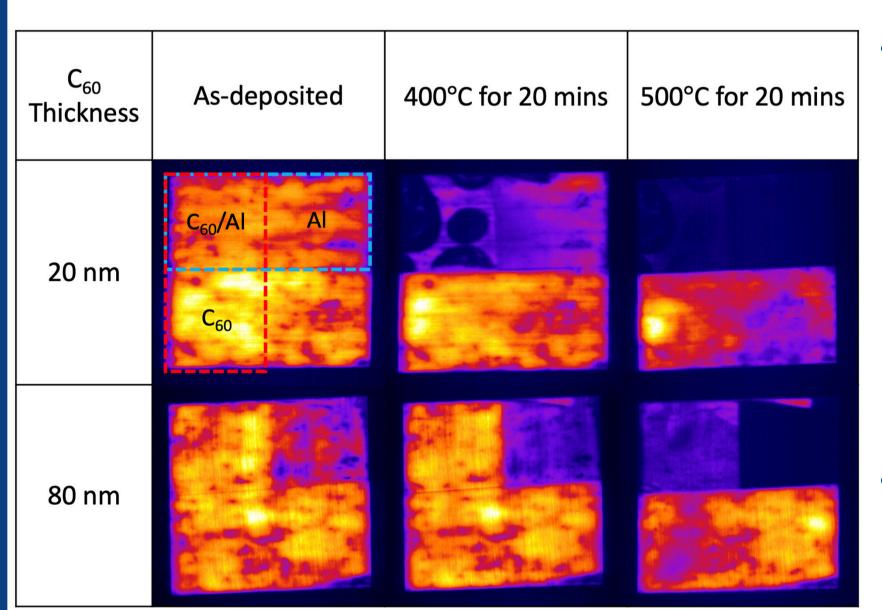


- $\rho_c$  of SiO<sub>2</sub>/n<sup>+</sup>poly-Si/C<sub>60</sub>/Al electron contacts as a function of annealing temperature with different C<sub>60</sub> thickness (20, 80 nm).
- $\rho_c$  for SiO<sub>2</sub>/n<sup>+</sup>poly-Si/Al without the C<sub>60</sub> layer are included for reference.
- $\rho_c$  does not improve with air exposure
- Even for 80 nm,  $\rho_c$  is low enough for fullarea contacts.



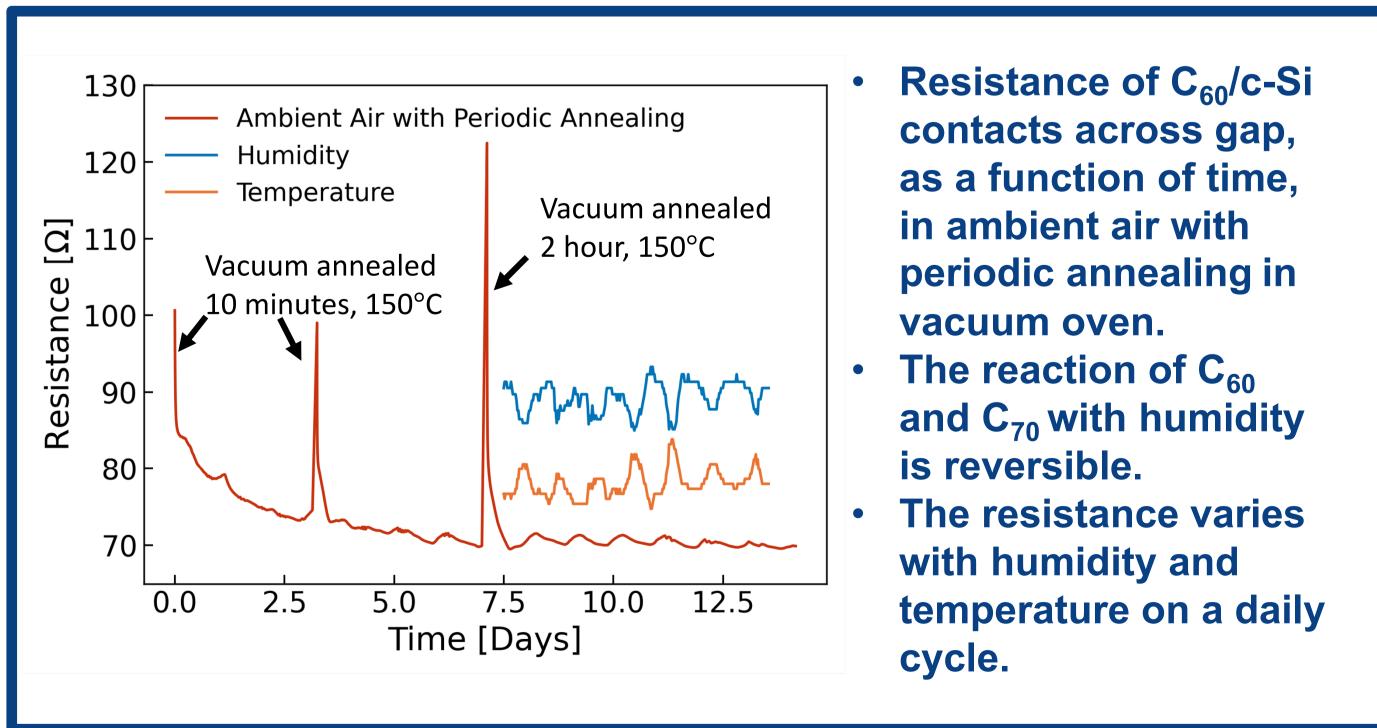
**Resistance of C<sub>60</sub>/c-Si** or C<sub>70</sub>/c-Si electron contacts across a fixed gap width, as a function of time in environments with different level of humidity.

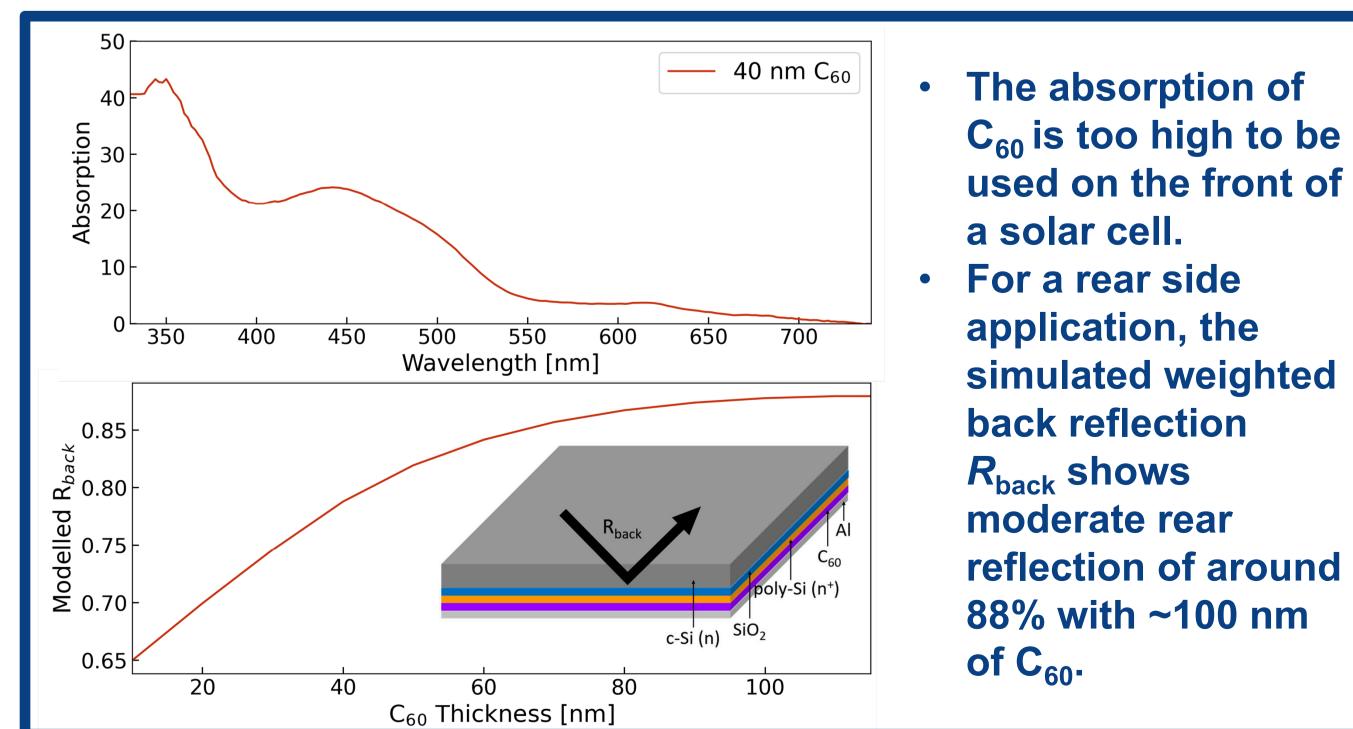
- Humidity seems to play a role in the decreasing of resistance.
- C<sub>70</sub> decreases faster than  $C_{60}$ .



**Photoluminescence** images of samples with  $C_{60}$  (20, 80 nm) and AI (~200 nm) thin films on top of poly-Si passivated electron contacts. Samples are stored in air.

80 nm of  $C_{60}$  can protect the sample up to 400°C.





simulated weighted reflection of around

University of Melbourne **Department of Electrical and Electronic Engineering** 

yuchen.sun@student.unimelb.edu.au james.bullock@unimelb.edu.au

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